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CONFIRMATION ☒

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**From:** Thomas T. Moga

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**Client Name:** Petronas  
**Client/Matter Number:** 46599-00301  
**Attorney Number:** [1114]  
**Secretary/Ext.:** Tina Tomayko/7545

**Matter Name:** patent application  
**Attorney Initials:** MAS  
**No. of Pages:** 17  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Confirmation No. 2100

OFFICIAL

Group Art Unit: 1765  
Examiner: Duy Vu Nguyen Deo  
Inventor(s): Chien-Wei Chen et al.  
Serial No.: 09/930,856  
Filing Date: August 16, 2001  
Title: METHOD FOR FORMING AN ELECTRICAL INSULATING LAYER  
ON BIT LINES OF THE FLASH MEMORY

Attorney Docket No. 46599-00301

AMENDMENT UNDER 37 C.F.R. §1.312

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby acknowledge receipt of the Notice of Allowance and Fee(s)

Due mailed November 19, 2003.

Please amend the above-identified patent application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Abstract** begin on page 14 of this paper.

**Remarks** begin on page 15 of this paper.